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Atty. Dkt. No. 040373-0382



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Koichi TERASHIMA, et al.  
Title: SEMICONDUCTOR DEVICE  
AND MANUFACTURING  
PROCESS THEREFOR

Appl. No.: 10/576,412

Filing Date: 4/19/2006

Examiner: Allan R. Wilson

Art Unit: 2815

Confirmation 3430  
Number:

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement set forth in the Office Action mailed September 17, 2008, Applicants hereby provisionally elect Group I, which corresponds to claims 1-8 and 20-22, drawn to a semiconductor device traverse.

Applicants reserve the right to file a divisional application to pursue the non-elected claims.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing or a credit card payment form being unsigned, providing incorrect

information resulting in a rejected credit card transaction, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741. If any extensions of time are needed for timely acceptance of papers submitted herewith, Applicant hereby petitions for such extension under 37 C.F.R. §1.136 and authorizes payment of any such extension fees to Deposit Account No. 19-0741.

Respectfully submitted,

Date October 17, 2008

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